

Electronics For Extreme Environments



Task Manager: Richard Patterson (GRC)

Objective

- Electrical Characterization test and report on selected PEM COTS devices for extreme environments,
- COTS device selection guideline for low-temperature/high temperature environments including degradation mechanisms and failure modes,
- Final report on identification of needed enabling technologies to improve operation, reliability, and lifetime of electronics at low temperatures.

Deliverables

- Extreme temperature electrical test results on selected plastic encapsulated microelectronic (PEM) COTS devices
- **■** COTS device selection guideline
- **■** Mid-year status report
- **■** Final Report

Participating Centers

■ Dr. Quie Kim (JPL)